

AMENDMENTS TO THE CLAIMS

Claims 1-38 (canceled)

Claim 39 (original): An encoding circuit for a CD-R or CD-RW recording apparatus, said encoding circuit comprising:

 a first memory;

 a second memory;

 a CD-ROM encoder for generating first encoded data, said first encoded data being stored in said first memory; and

 a CIRC encoder for generating second encoded data, said second encoded data being stored in said second memory; and

wherein said encoding circuit supports a pause condition which pauses said CD-ROM encoder and said CIRC encoder while maintaining the contents of said first and second memories.